

FIG. 1

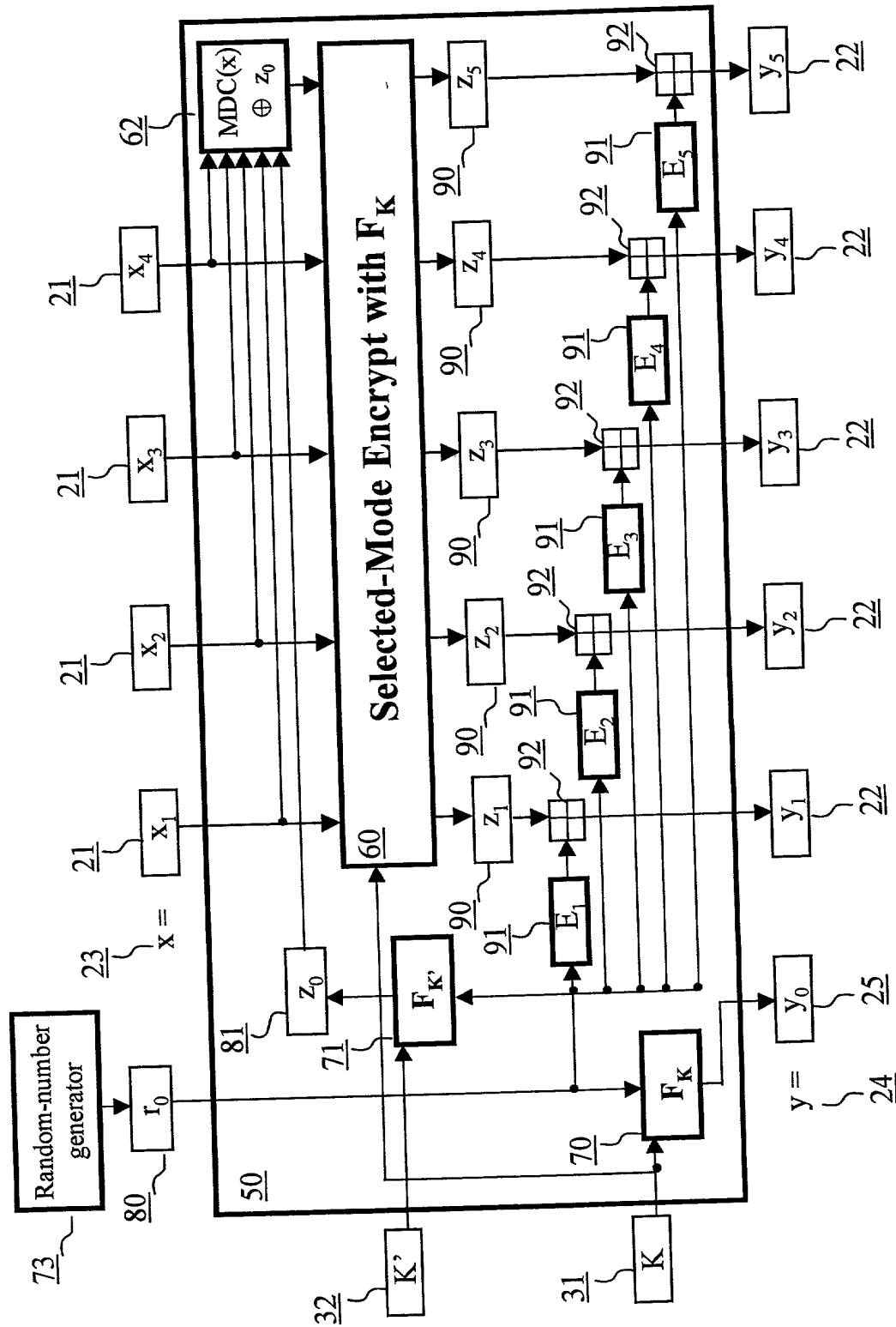
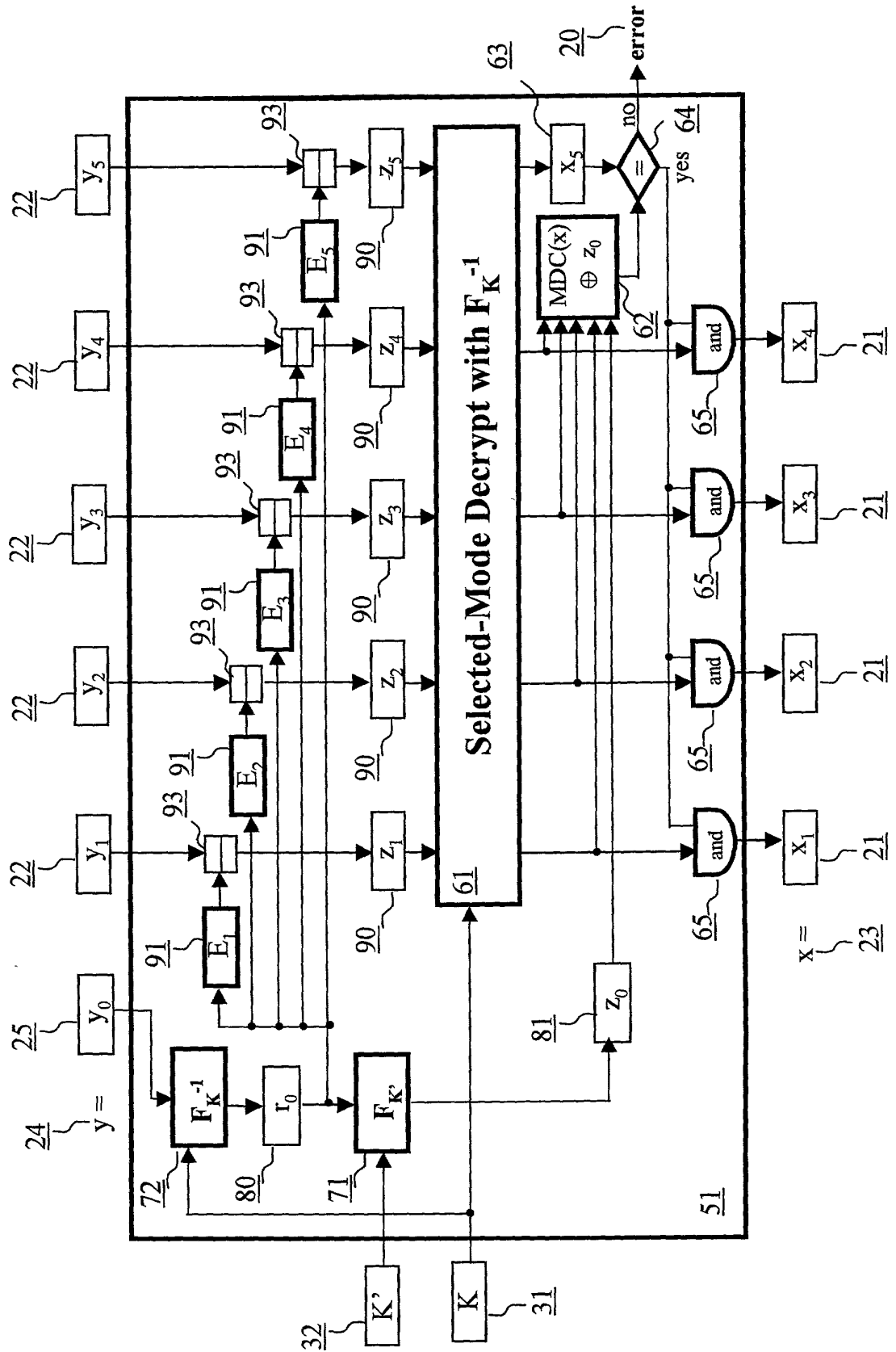
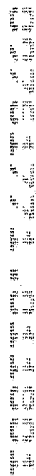


FIG. 2



[illegible]

The diagram illustrates a decryption process. A ciphertext vector $y = [y_0, y_1, y_2, y_3, y_4, y_5]$ is input to a decryption block. y_0 is processed by F_K^{-1} to produce r_0 , which is then added to a constant c to produce $r_0 + c$. This value is processed by F_K to produce z_0 . The remaining ciphertext elements y_1 through y_5 are each processed by a corresponding encryption block E_1 through E_5 , which also take z_0 as input. The outputs of these blocks are z_1 through z_5 . These z values are then fed into a 'Selected-Mode Decrypt with F_K^{-1} ' block. The outputs of this block are x_1 through x_4 . x_1 through x_4 are each ANDed with z_0 to produce the final plaintext outputs x_1, x_2, x_3, x_4 . An error detection block takes x_5 and z_0 as input and outputs an error signal.

$$x \sim \underline{23}$$

FIG. 5

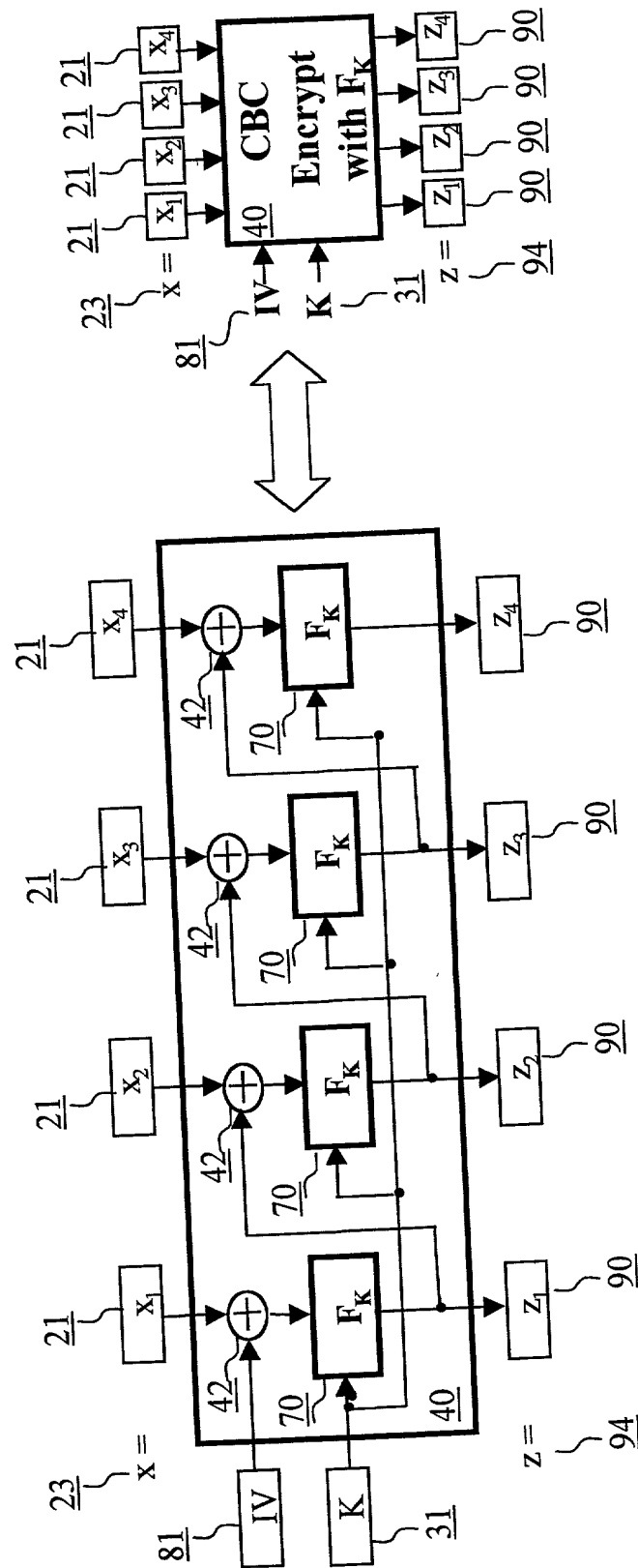


FIG. 6

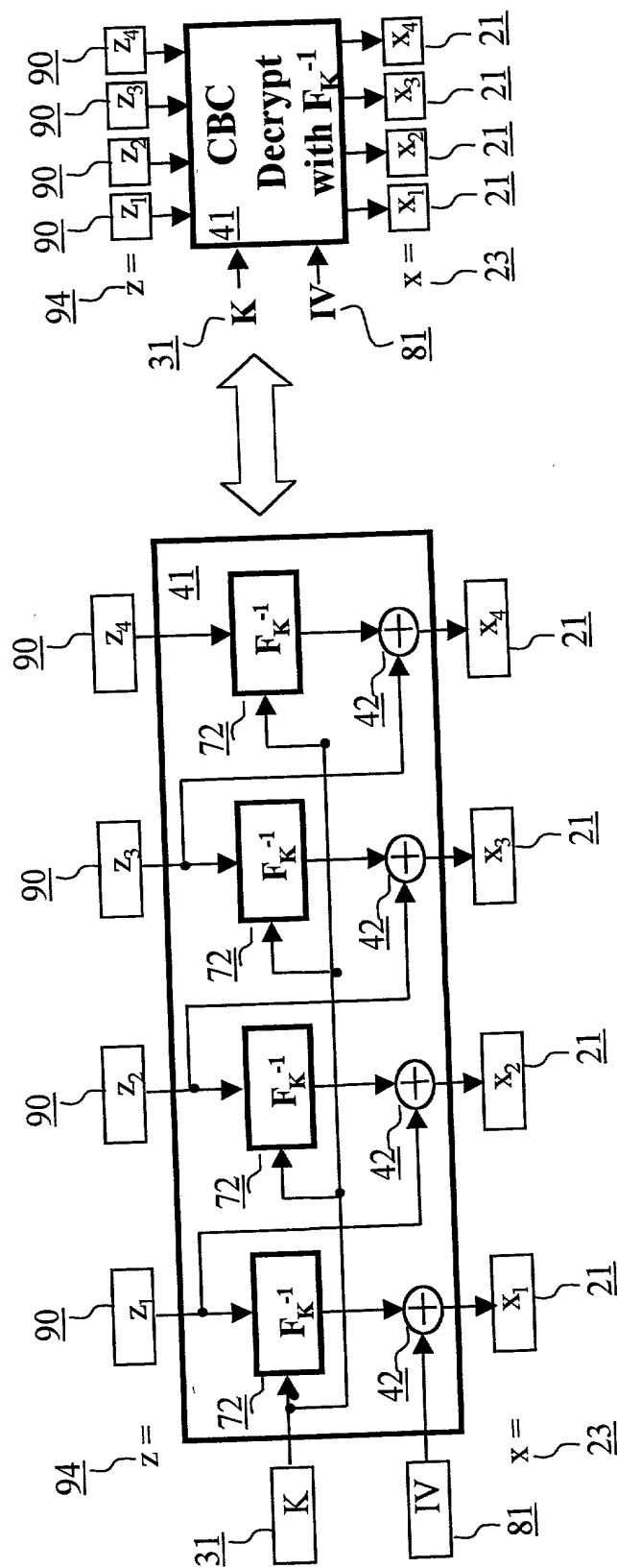


FIG. 7

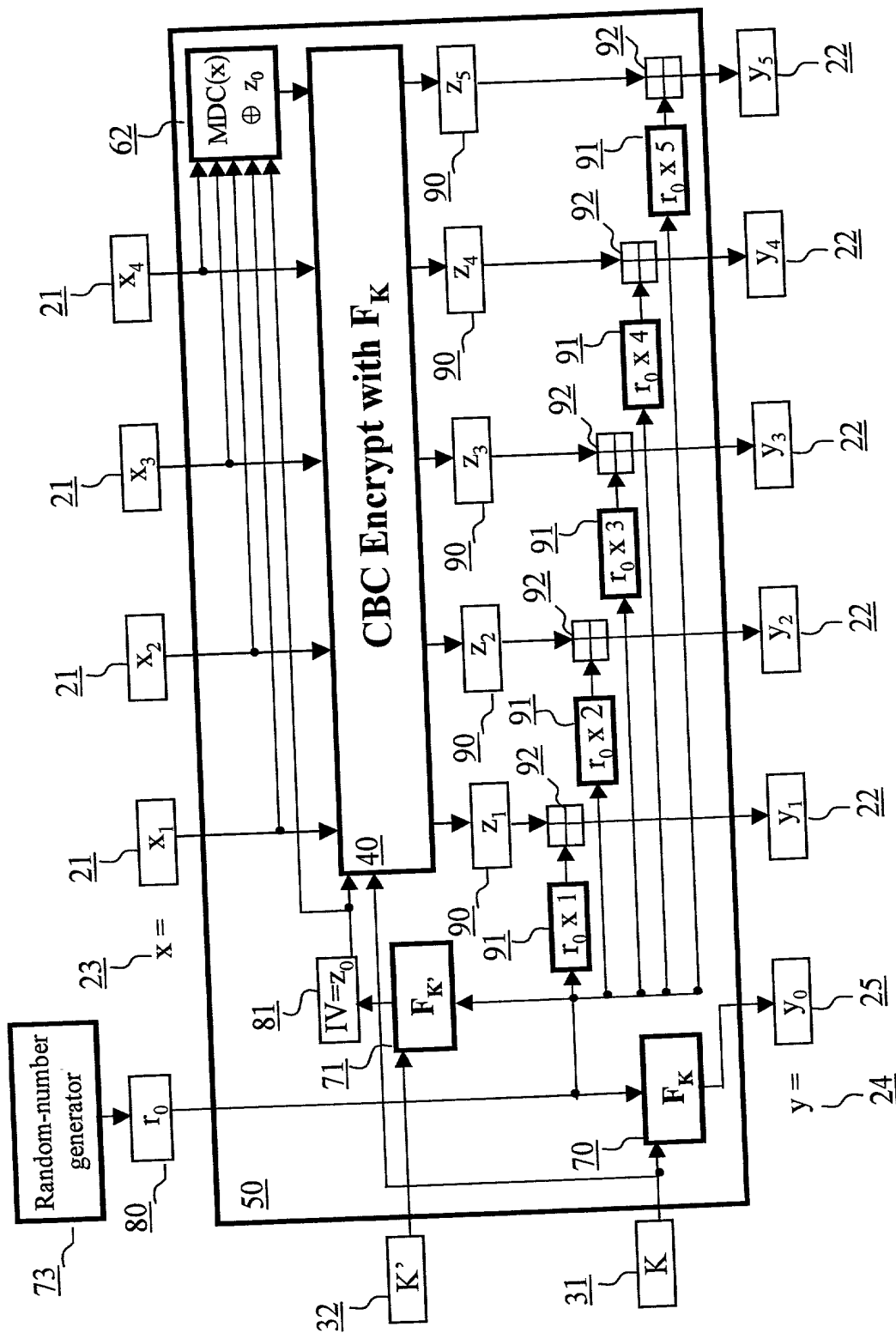


FIG. 8

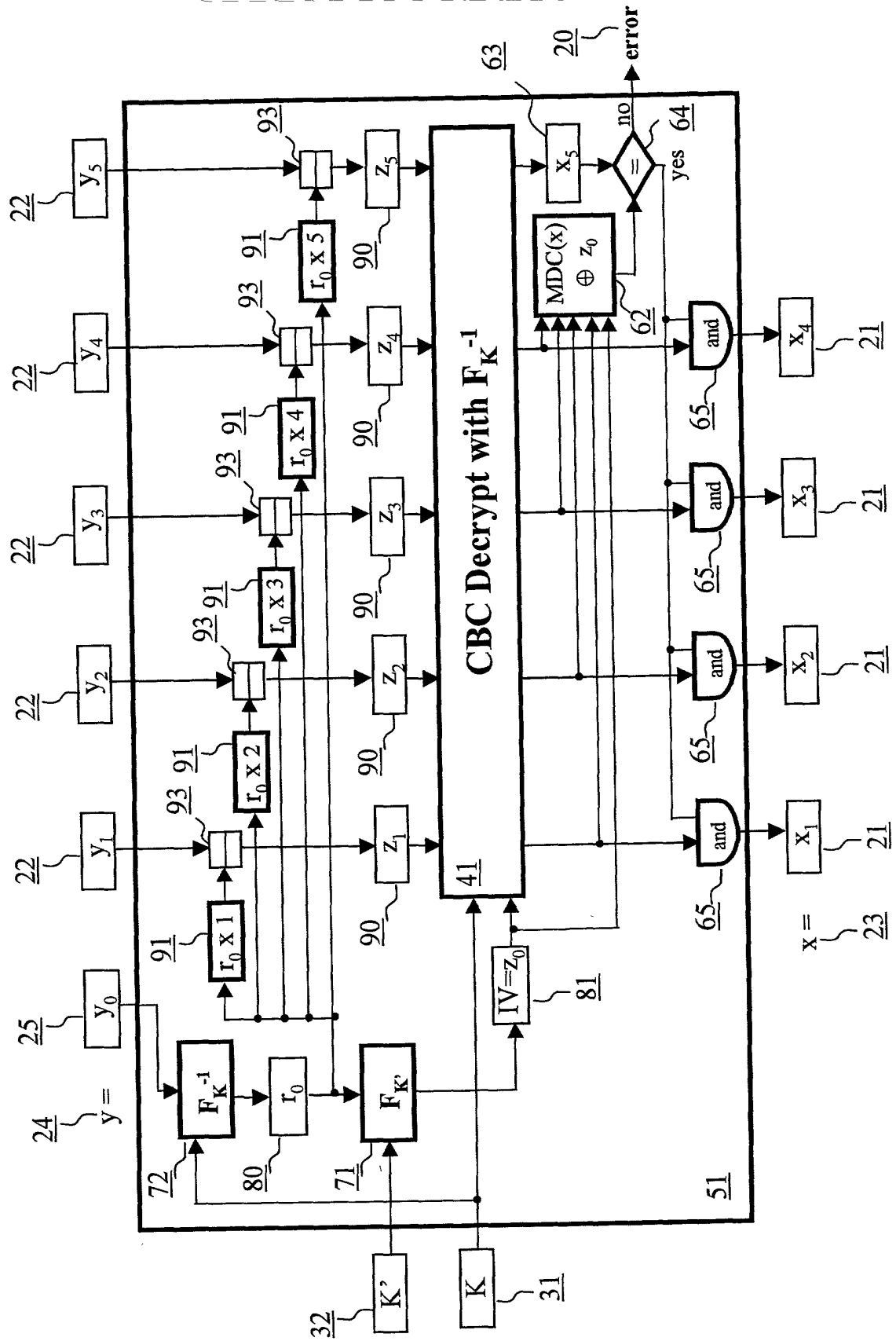


FIG. 9

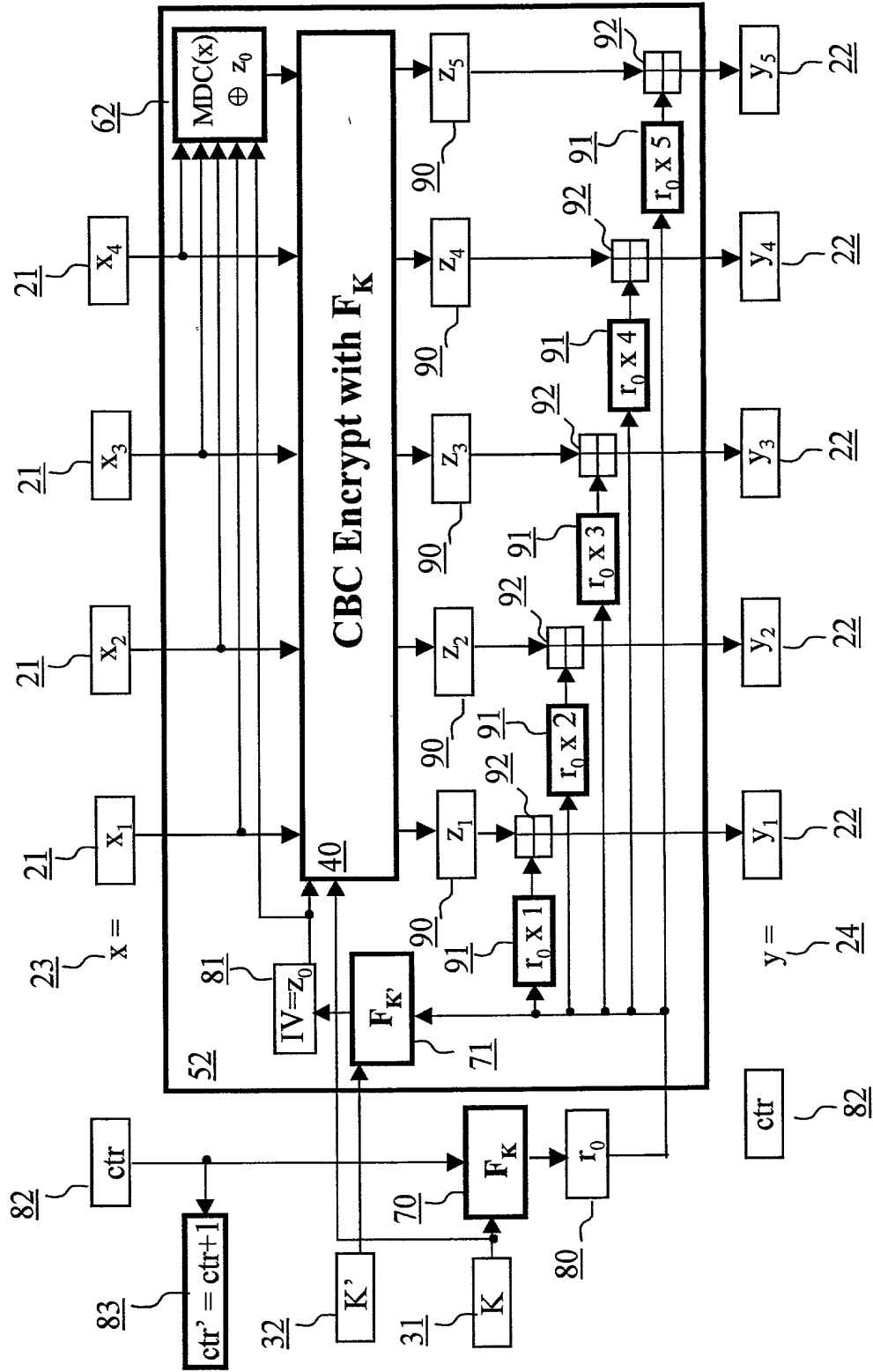


FIG. 10

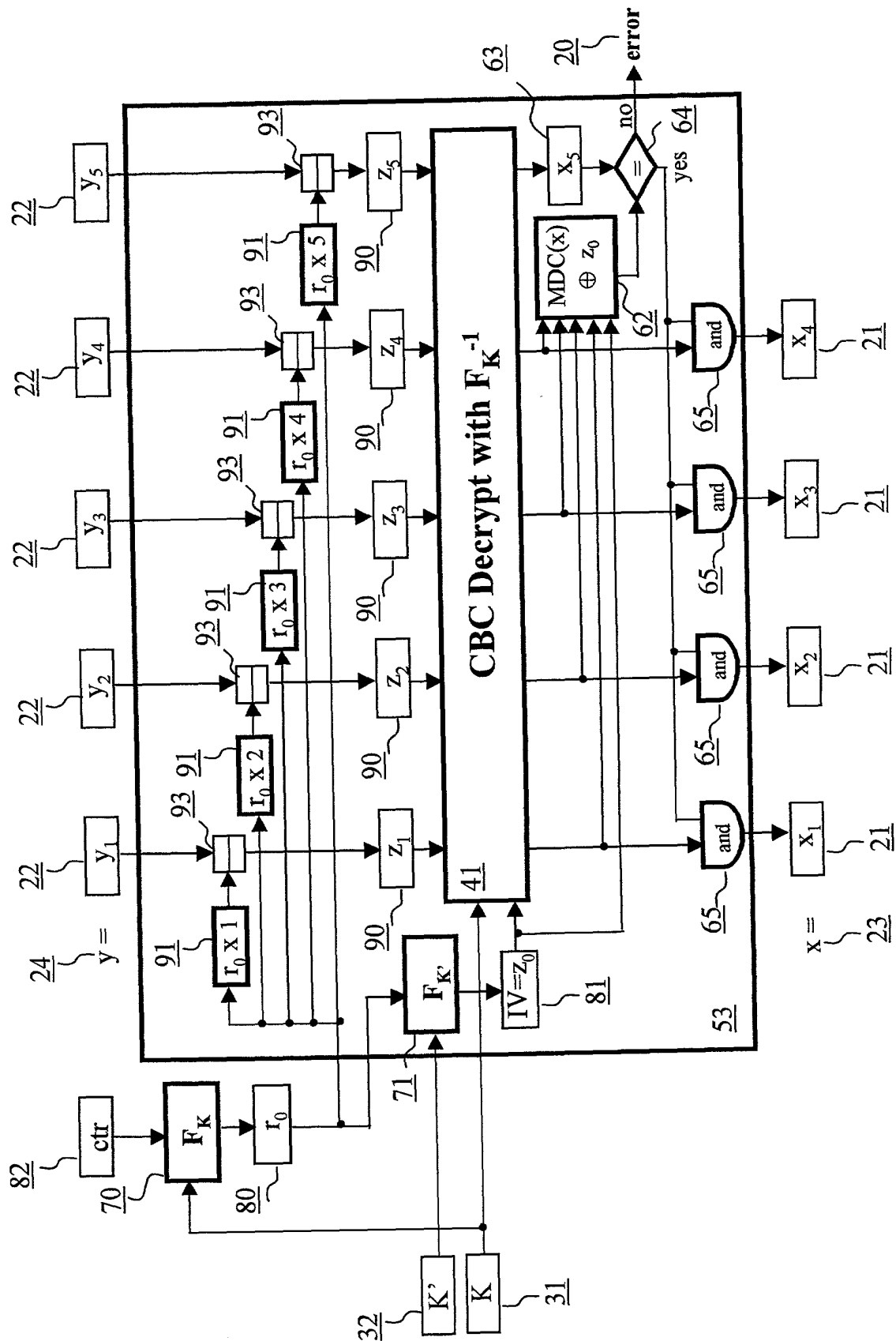


FIG. 11

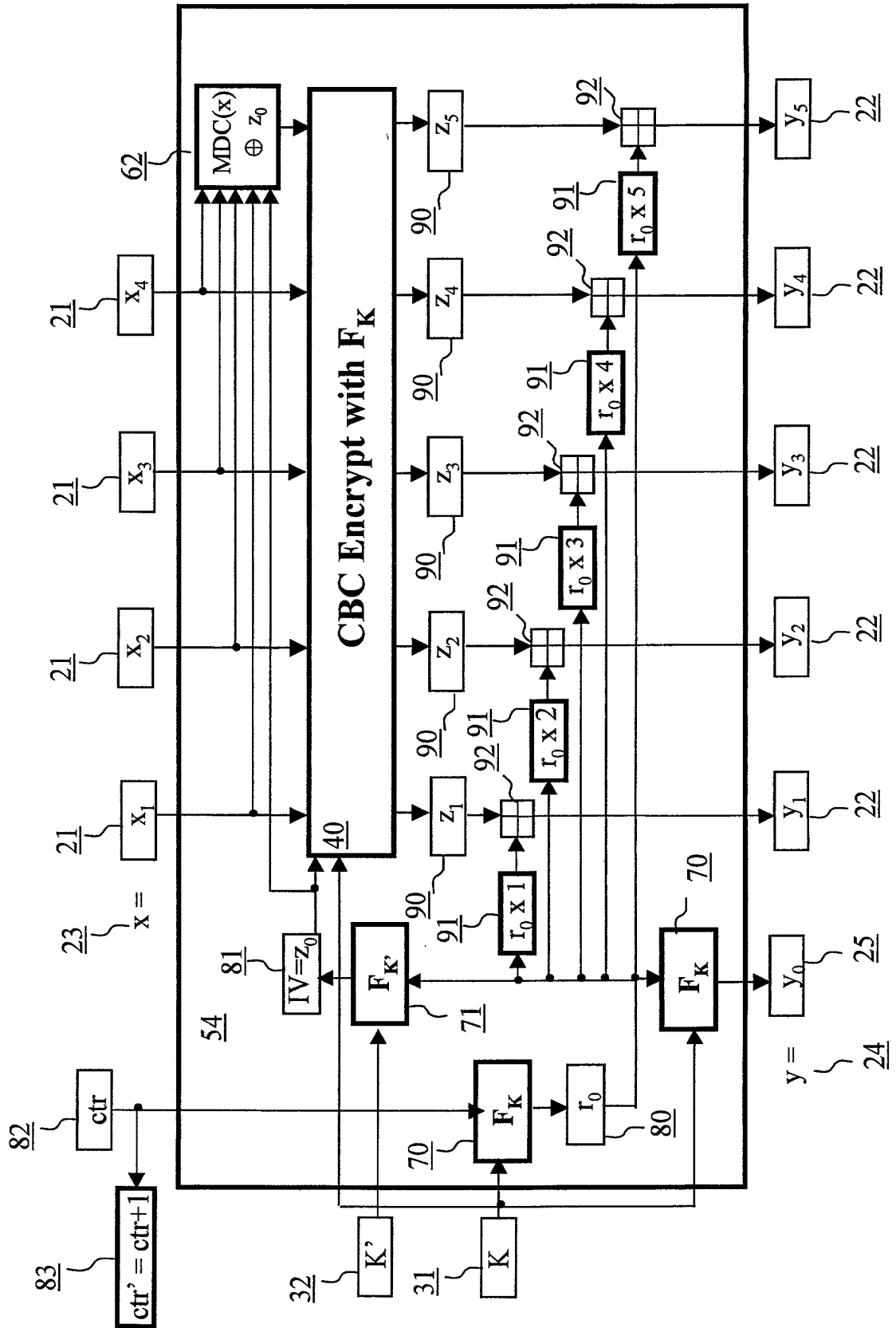


FIG. 12

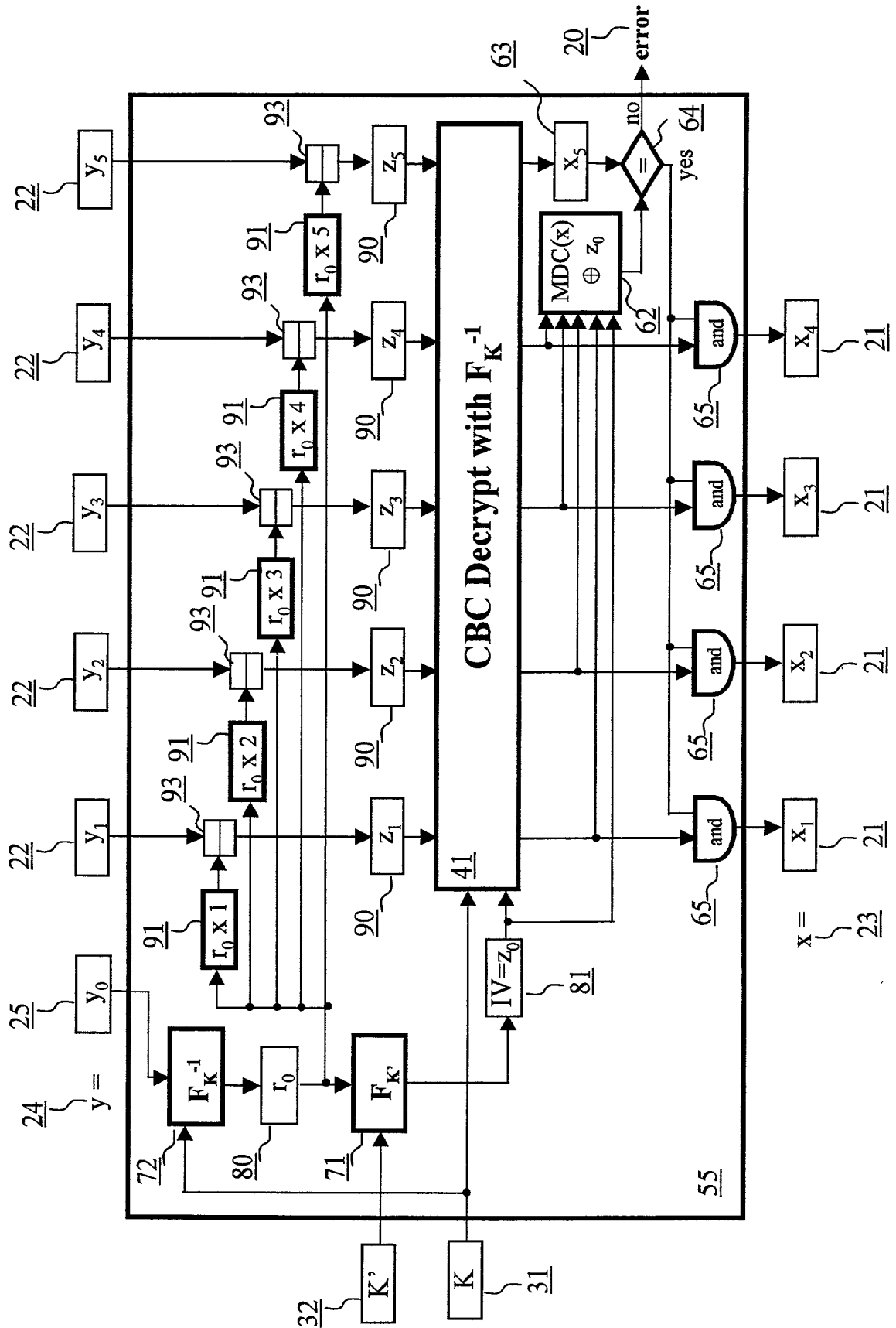


FIG. 13

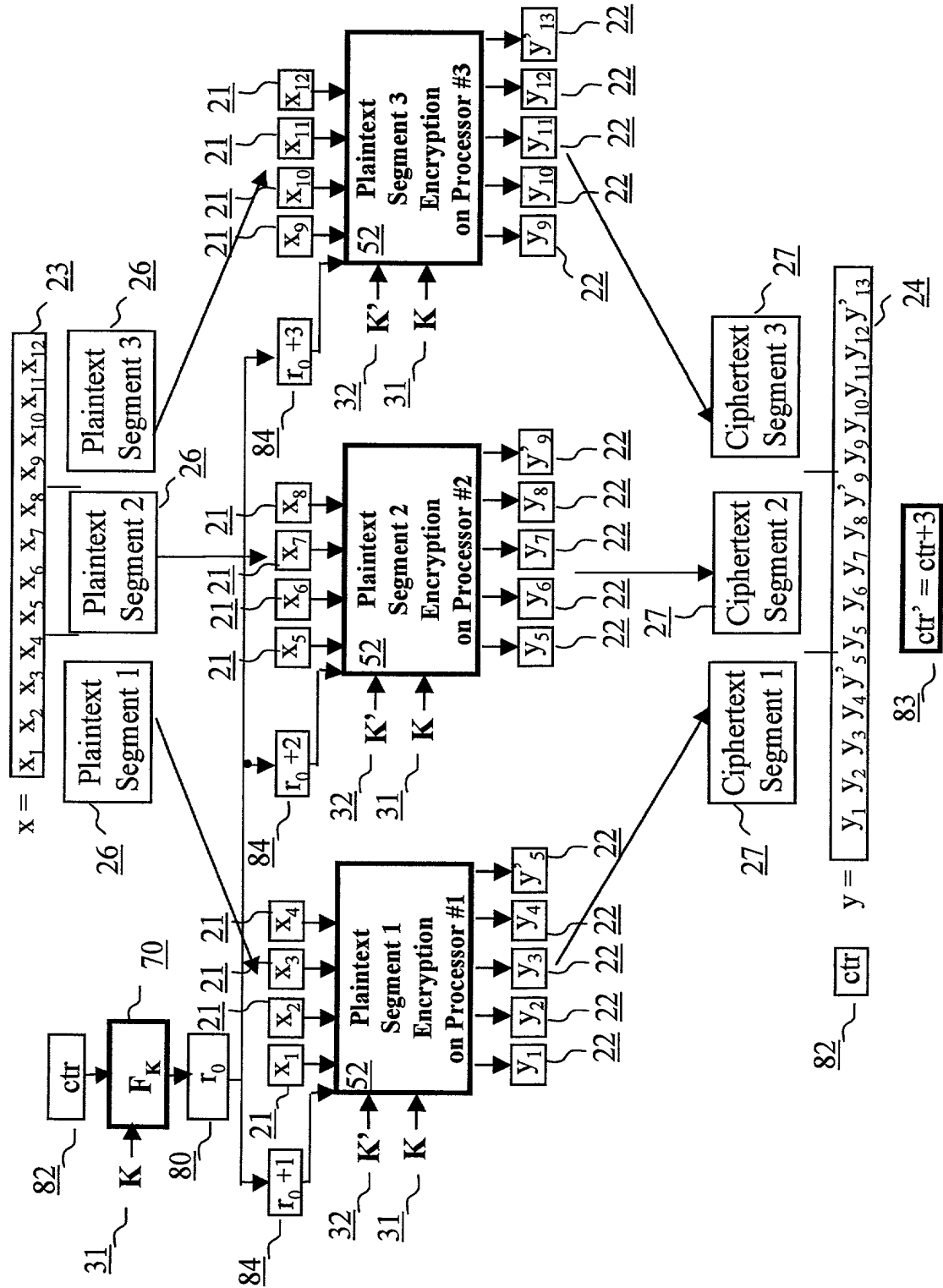


FIG. 14

